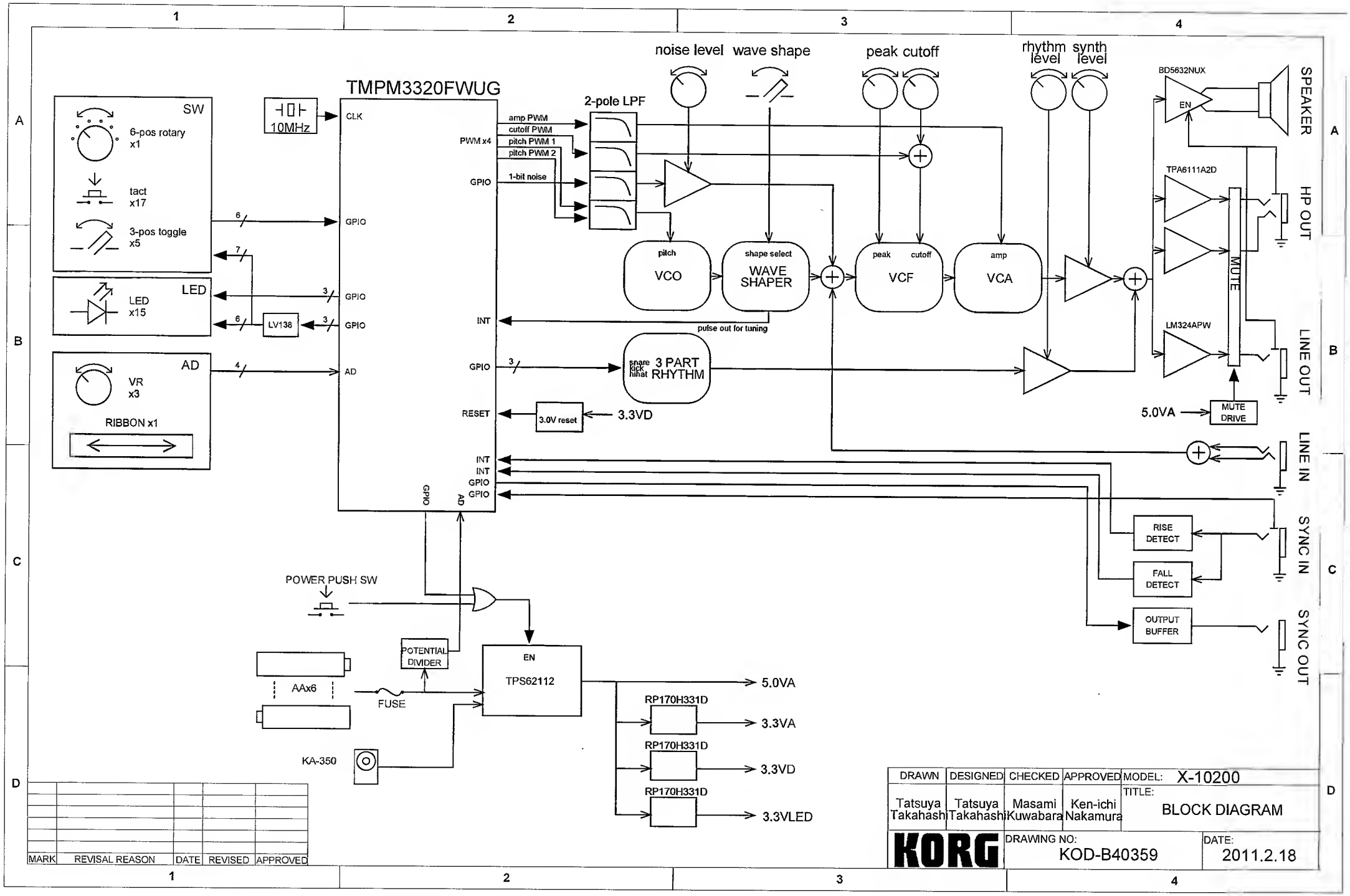
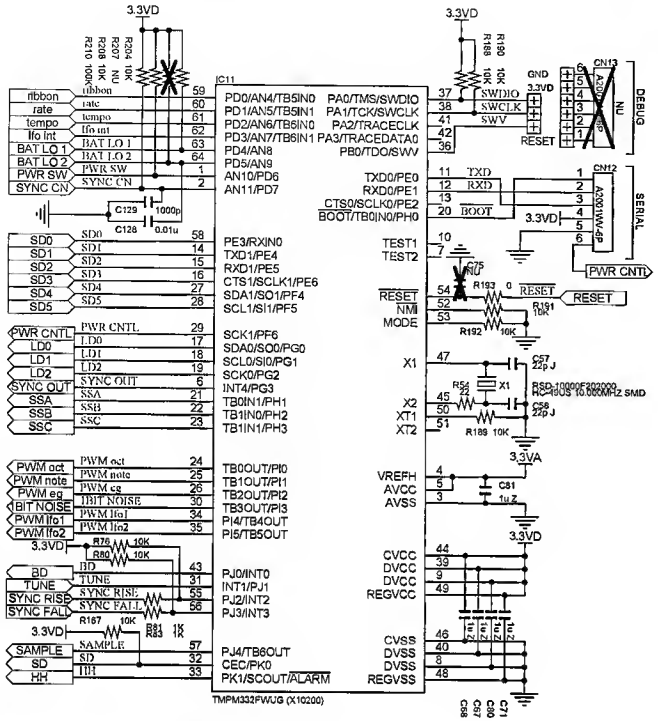


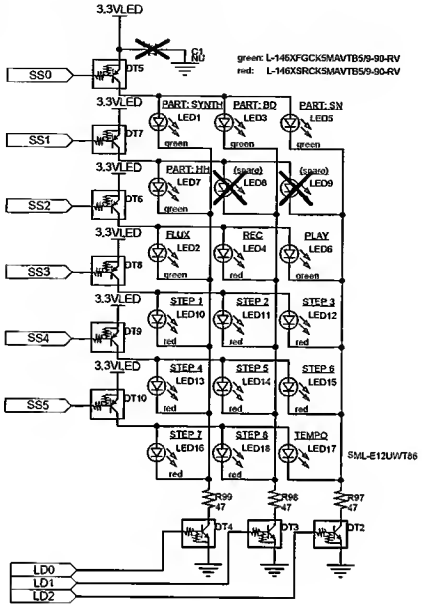
MARK	REVISAL REASON	DATE	REVISED	APPROVED

DRAWN	DESIGNED	CHECKED	APPROVED	MODEL: X-10200
Tatsuya Takahashi	Tatsuya Takahashi	Masami Kuwabara	Ken-ichi Nakamura	TITLE: BLOCK DIAGRAM
KORG		DRAWING NO: KOD-B40359		DATE: 2011.2.18

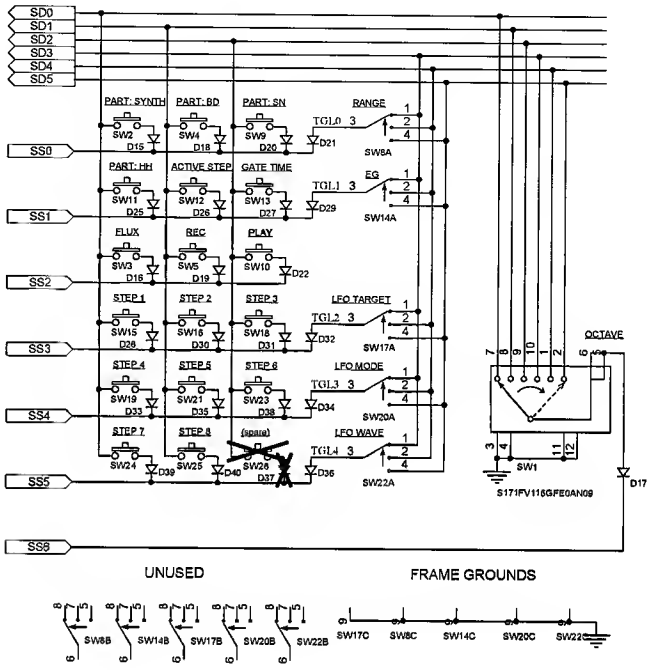
MICROPROCESSOR

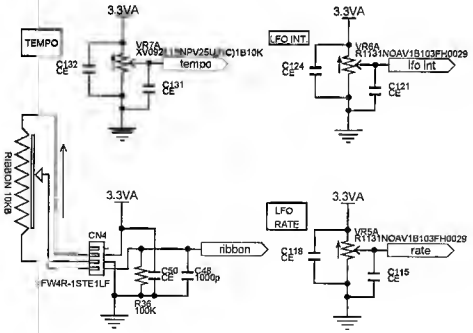


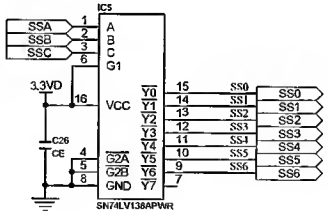
LED MATRIX



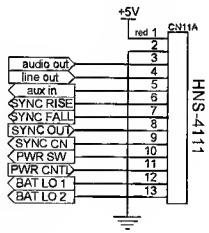
SWITCH MATRIX







TO JACK PCB



dummy

VR5C — dummy

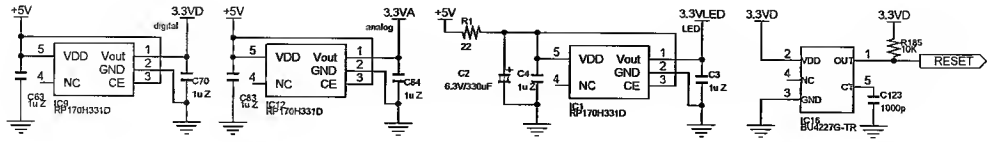
VR6C — dummy

frame grounds

VR5B

VR7B

VR6B



All ceramic capacitors are K rank unless specified otherwise

CE: 0.1uF Z
D: 1SS355ST(A)
L: BLM18BD102SN1D (S)
WD: MC2840-T112-1 (S)
NPN: 2SC1623-T1B-A<L6/L7>(S)
PNP: 2SA812-T1B-A M5-7 M6 RANK(S)
FET: 2SK433-T112-1E (S)

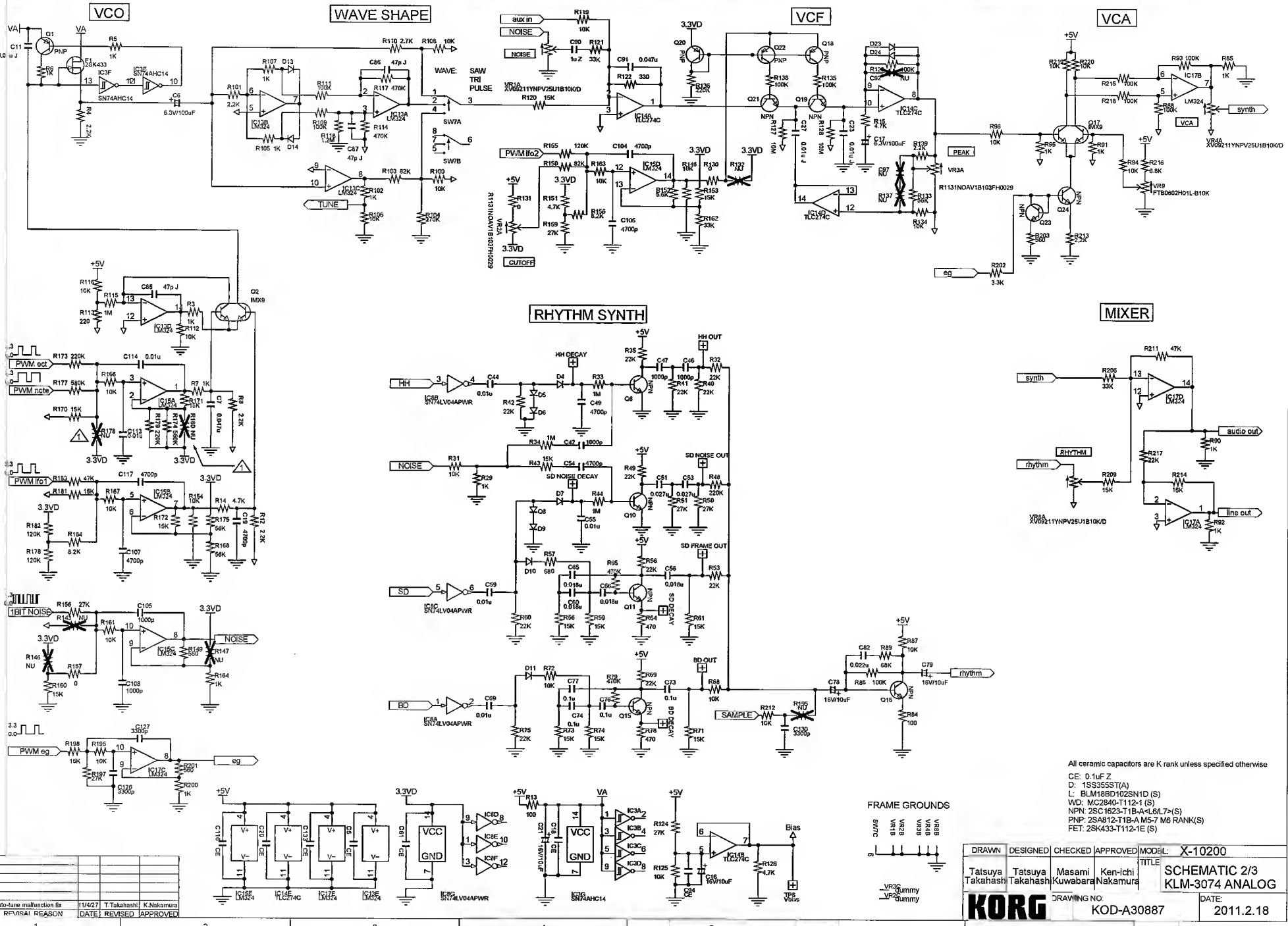
MARK	REVISAL REASON	DATE	REVISED	APPROVED

DRAWN	DESIGNED	CHECKED	APPROVED	MODEL: X-10200
Tatsuya Takahashi	Tatsuya Takahashi	Masami Kuwabara	Ken-ichi Nakamura	TITLE: SCHEMATIC 1/3 KLM-3074 DIGITAL

KORG

DRAWING NO: KOD-A30886

DATE: 2011.2.18

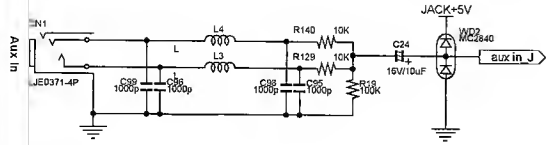


MARK	REVISAL REASON	DATE	REVISED	APPROVED
⚠	auto-tune malfunction fix	11/4/27	T.Takahashi	K.Nakamura

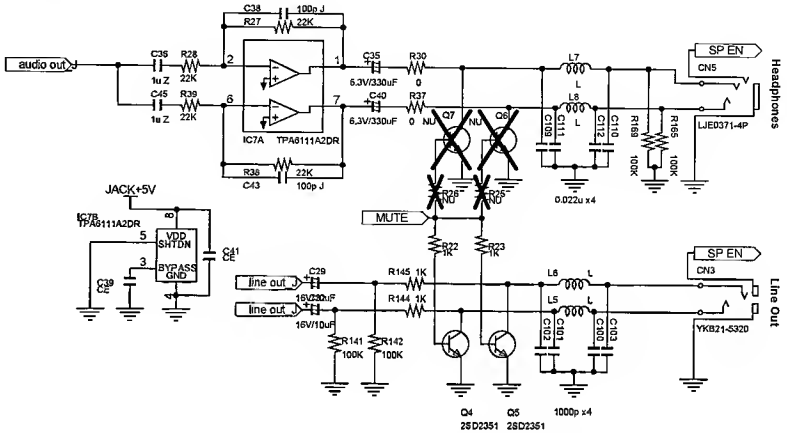
DRAWN	DESIGNED	CHECKED	APPROVED	MODEL: X-10200
Tatsuya Takahashi	Tatsuya Takahashi	Masami Kuwabara	Ken-ichi Nakamura	TITLE: SCHEMATIC 2/3 KLM-3074 ANALOG

KORG DRAWING NO: KOD-A30887 DATE: 2011.2.18

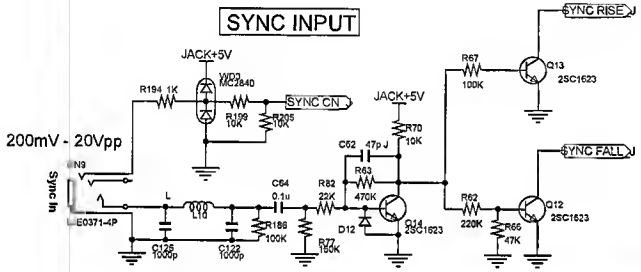
AUX INPUT



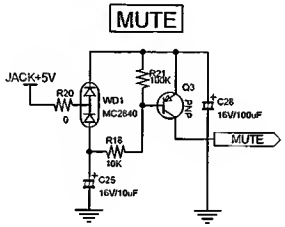
HP AMP



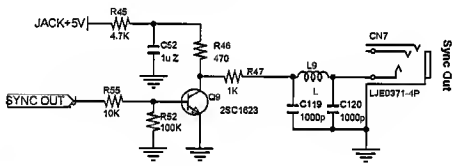
SYNC INPUT



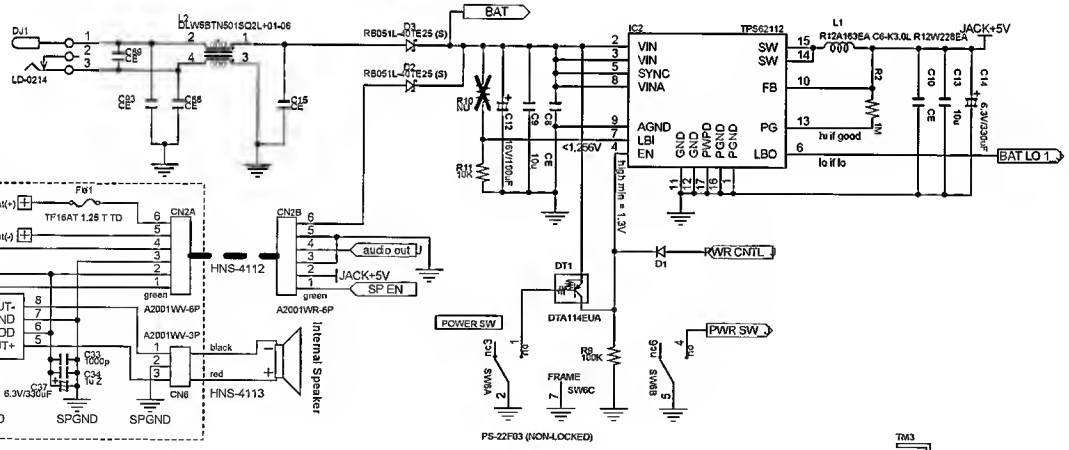
MUTE



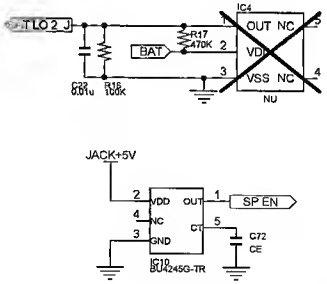
SYNC OUTPUT



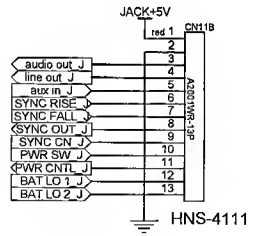
5V POWER SUPPLY

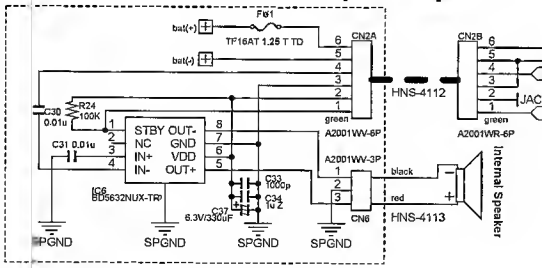


VOLTAGE DETECTOR

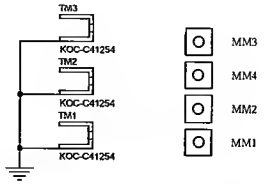


TO PANEL





KLM-3076
BATTERY PCB



MM3
MM4
MM2
MM1

All ceramic capacitors are K rank unless specified otherwise

CE: 0.1uF Z
D: 1SS355ST(A)
L: BLM18BD102SN1D (S)
WD: MC2840-T112-1 (S)
NPN: 2SC1623-T1B-A<L6L7>(S)
PNP: 2SA812-T1B-A M5-7 M6 RANK(S)
FET: 2SK433-T112-1E (S)

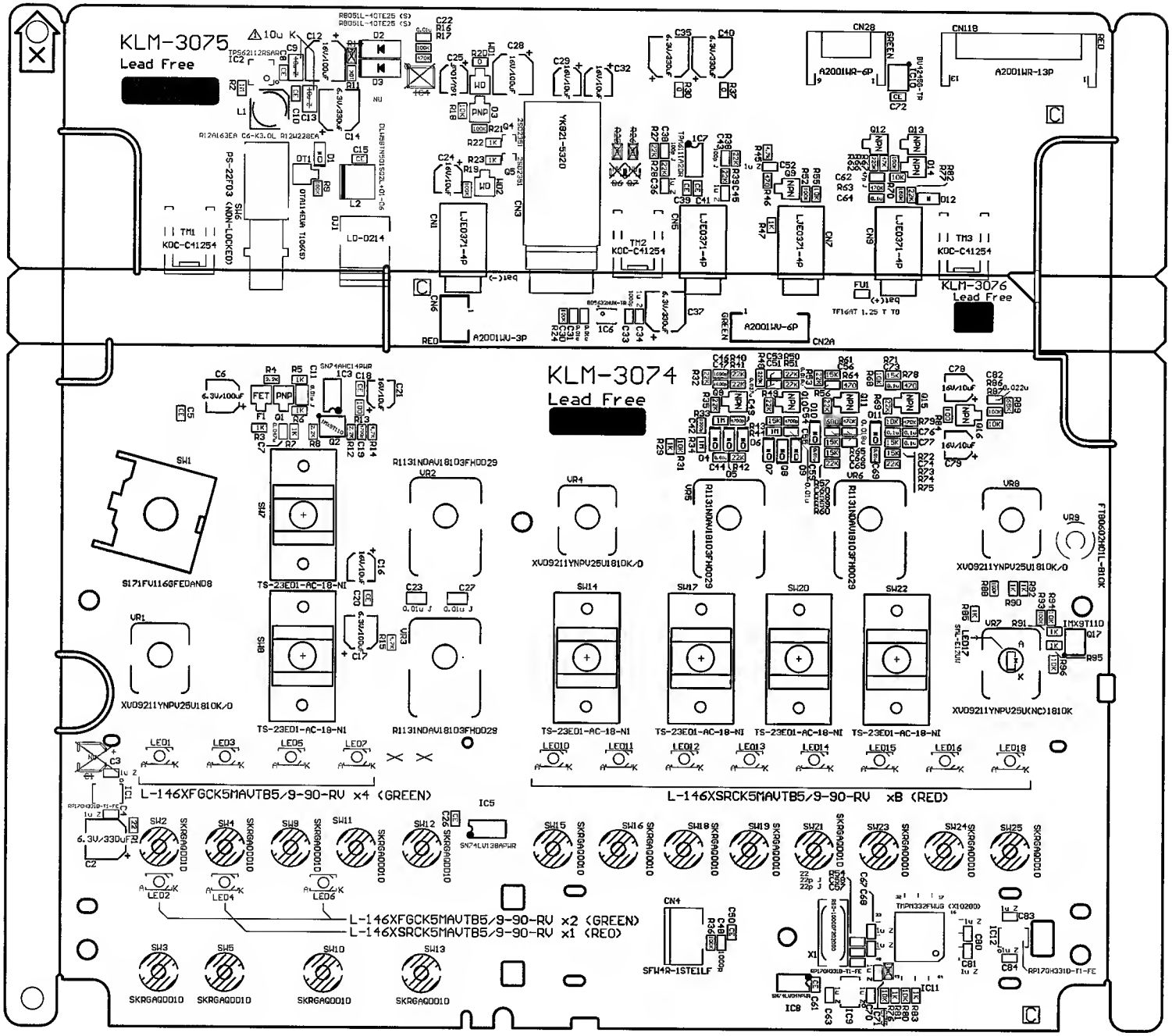
MARK	REVISAL REASON	DATE	REVISED	APPROVED

DRAWN	DESIGNED	CHECKED	APPROVED	MODEL: X-10200
Tatsuya Takahashi	Tatsuya Takahashi	Masami Kuwabara	Ken-ichi Nakamura	TITLE: SCHEMATIC 3/3 KLM-3075 JACK KLM-3076 BATTERY

KORG

DRAWING NO.: KOD-A30888

DATE: 2011.2.18



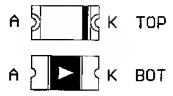
RP170H3310-T1-FE

L-146XFGCK5MAVTB5/9-90-RV (GREEN)
L-146XSRCK5MAVTB5/9-90-RV (RED)

DLW5BTN501SQ2L+01-06

observe marking

SML-E12UW



SYMBOLS:

CE: 0.1uF Z
D: 1SS355ST(A)
WD: MC2840-T112-1 (S)
NPN: 2SC1623B-T1B-AT L6/L7 RANK
PNP: 2SAB12B-T1B-AT M6 RANK
FET: 2SK433-T112-1E (S)

All ceramic capacitors are K rank unless specified.

Ensure all lead components are fully inserted.

Switch levers and VR knobs must be free of scratches, flux or any other markings.

Please cut excess leads of LEDs and tact switches after dip soldering.

セラミックコンデンサーは指定が無い場合はKランク を使用。

リード部品に浮き、傾きが無いこと。

スイッチレバー、VRツマミにフラックス等のの汚れや傷が無いこと。

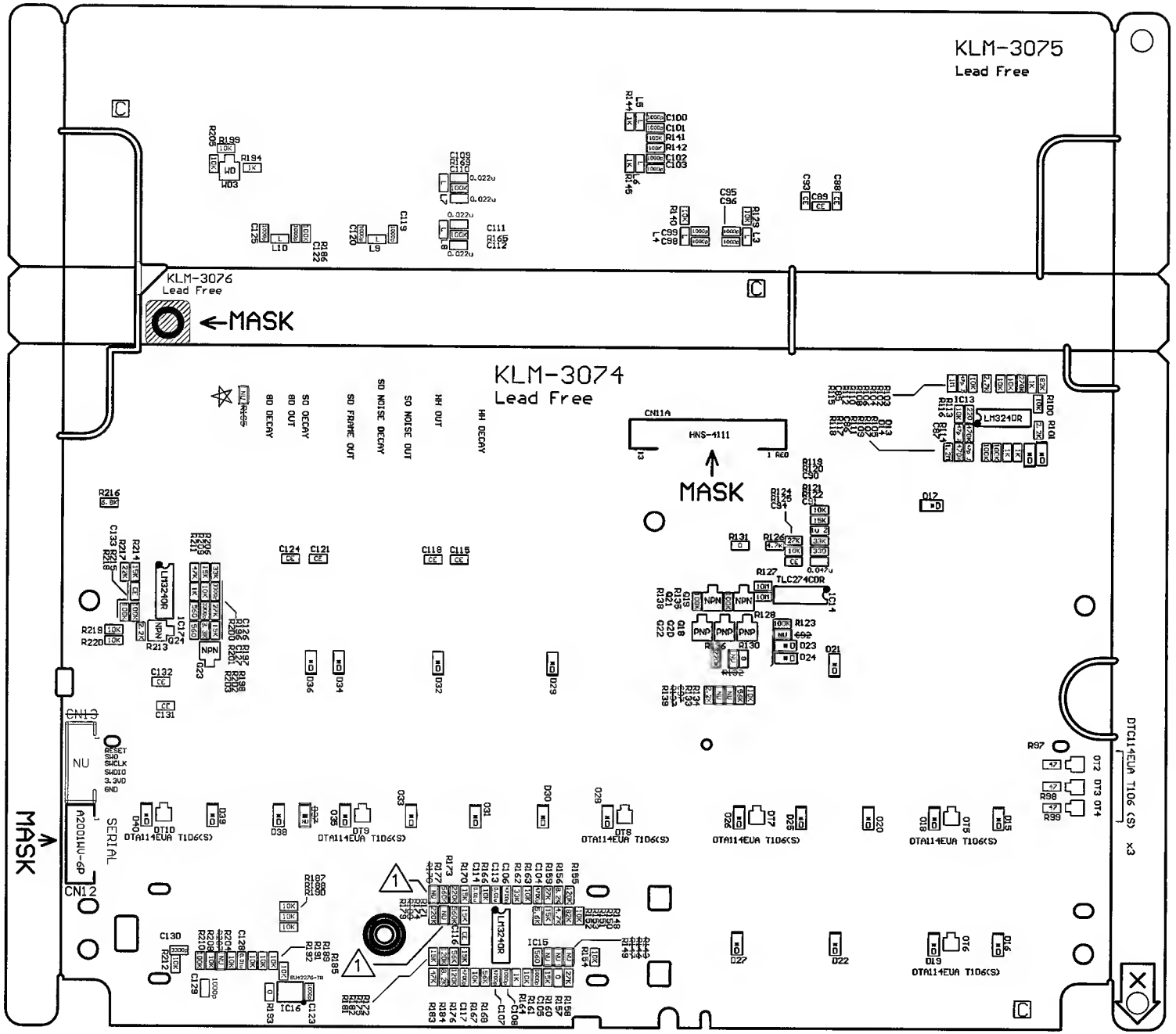
LED とタクトスイッチのリードをディップ後にカットして下さい。

MARK	REVISAL REASON	DATE	REVISED	APPROVED
⚠	change ceramic C spec	2011.6.10	TAKAHASHI	NAKAMURA

DESIGNED	DRAWN	CHECKED	APPROVED	MODEL
TATSUYA TAKAHASHI	TATSUYA TAKAHASHI	MASAMI KUWABARA	KEN-ICHI NAKAMURA	X-10200

TITLE: KLM-3074/5/6 MOUNT DIAGRAM TOP

KORG | DRAWING No. KOD-F30950 | C | DATE 2011.02.24



SYMBOLS:

CE: 0.1uF Z
L: BLM18BD102SN1D (S)
D: 1SS355ST(A)
WD: MC2840-T112-1 (S)
NPN: 2SC1623B-T1B-AT L6/L7 RANK
PNP: 2SA812B-T1B-AT M6 RANK

All ceramic capacitors are K rank unless specified.

Mask all areas marked MASK before dip soldering: under CN11A, CN12 and over hatched area

Hand solder CN11A and CN12 after dip soldering

セラミックコンデンサーは指定が無い場合はKランク を使用。

MASKと指示されている部分はマスキングにてディップして下さい： CN11A、CN12、斜線部

CN11A、CN12 はディップ後にマウントし手ハンダして下さい

MARK	REVISAL REASON	DATE	REVISED	APPROVED
1	auto-tune malfunction fix	2011.04.27	Takahashi	Nakamura

DESIGNED	DRAWN	CHECKED	APPROVED	MODEL
TATSUYA TAKAHASHI	TATSUYA TAKAHASHI	MASAMI KUWABARA	KEN-ICHI NAKAMURA	X-10200

TITLE: KLM-3074/5/6 MOUNT DIAGRAM BOT

KORG | DRAWING No. KOD-G30335 | C | DATE 2011.02.24